

Notice of References Cited	Application/Control No. 10/644,158	Applicant(s)/Patent Under Reexamination TOUTOUNCHI ET AL.	
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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	V	Rajsuman, R., "IDDQ testing for CMOS VLSI", 4/2000. IEEE, pgs. 544-568.			
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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